

**Amendments to the Specification:**

Please replace the paragraph beginning at page 4, line 19, with the following rewritten paragraph:

-- In fig. 2 is shown a schematic drawing of a similar scenario as shown in fig. 1, but now provided with a virtual link handler 100 having two new processes P1' and P2', respectively, and driver adapters according to the present invention, to allow signal communication between the processes. In systems wherein the present invention is implemented and wherein different types of processors are utilized, the system designer must take into consideration the use of formats of the various processors, such as byte ordering and padding. --